



FORM TDP-1449 (Based on Form PTO-1449)

PATENT AND TRADEMARK OFFICE
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Sheet 1 of 2

ATTORNEY DOCKET NO.

MP0426

SERIAL NO.

10/814,534

APPLICANT

Aram, Farbod

FILING DATE

March 31, 2004

GROUP

2817

U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	H. C.	6,466,091 B1	10/15/2002	Kejariwal, Murari et al	—	
2.	H. C.	6,515,540 B1	2/4/2003	Prasad, Ammisetti et al	—	
3.	H. C.	6,525,589 B1	2/25/2003	Thomsen, Axel et al	—	

FOREIGN PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes	No
1.							

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	
1.	H. C.	W. Grise, Dept. of IET, Morehead State University, Morehead, KY; Application of the Operational Transconductance Amplifier (OTA) to Voltage-controlled Amplifiers and Active Filters; 10 pages
2.	H. C.	Axel Thomsen, Dan Kasha, Wai Lee, Crystal Semiconductor Division, Cirrus Logic, Inc., Austin, Texas; A Five Stage Chopper Stabilized Instrumentation Amplifier Using Feedforward Compensation; 1998; 4 pages
3.	H. C.	Axel Thomsen, Edwin de Angel, Sherry Ziachong Wu, Aryesh Amar, Lei Wang, Wai Lee, WA 20.1 A DC Measurement IC with 130nV _{pp} Noise in 10Hz; 2000 IEEE International Solid-State Circuits Conference; 9 pages
4.	H. C.	Axel Thomsen, Edwin de Angel, Sherry Ziachong Wu, Aryesh Amar, Lei Wang, Wai Lee, WA 20.1 A DC Measurement IC with 130nV _{pp} Noise in 10Hz; ISSCC 2000/Session 20/Oversampled Converters/ Paper WA 20.1 ISSCC 2000 Slide Supplement; 5 pages

Examiner:

Henry Choe

Date Considered:

9/24/05

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Sheet 2 of 2

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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	
5.	H. C.	M. Moyal, M. Groepl, H. Werker, G. Mitteregger, J. Schambacher; Xignal Technologies AG, Munich, Germany; 23.6 A 700/900mW/Channel CMOS Dual Analog Front-End IC for VDSL with Integrated 11.5/14.5dBm Line Drivers; 2003 IEEE International Solid-State Circuits Conference; ISSCC 2003/Session 23/Mixed-Signal and Wireline Techniques; 10 pages
6.	H. C.	M. Moyal, M. Groepl, H. Werker, G. Mitteregger, J. Schambacher; Xignal Technologies AG, Munich, Germany; A 700/900mW/Channel CMOS Dual Analog Front-End IC for VDSL with Integrated 11.5/14.5dBm Line Drivers; Outline, Overview 24 pages

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